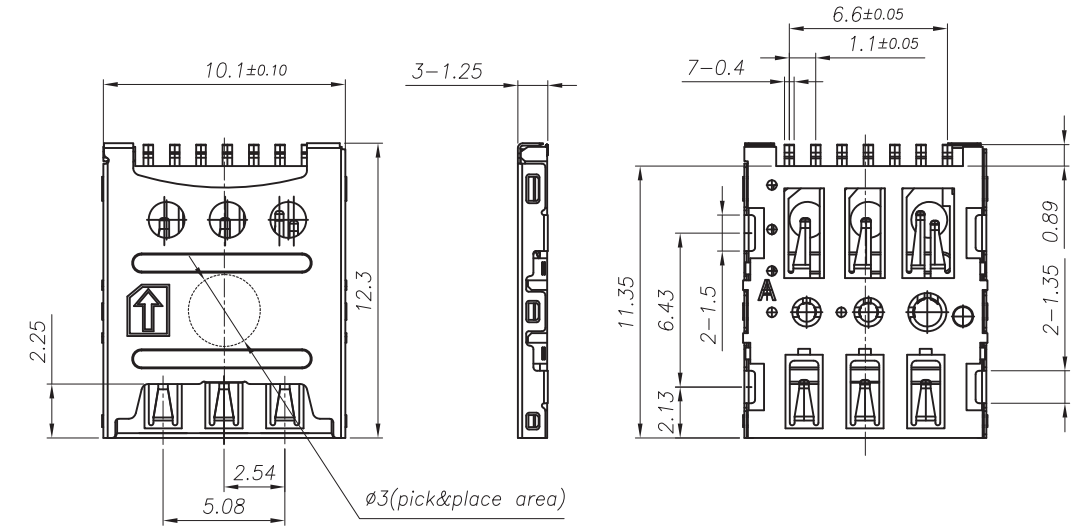
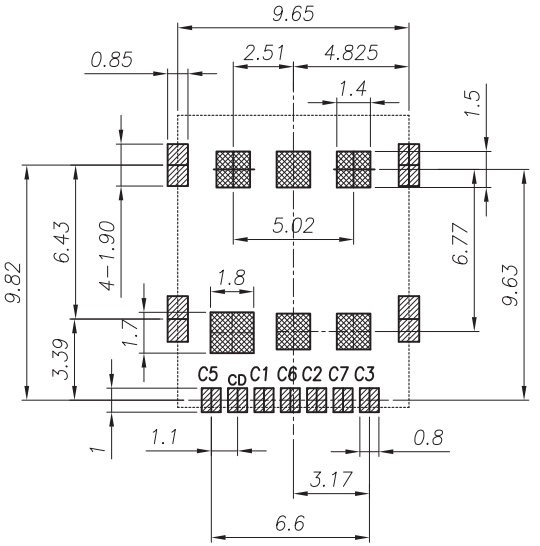
REV.	ECN NO OR DESCRIPTION	REVISED	DATE
1	PROPOSE ONLY	ALLEN	2015.03.04



SW Circuit Description for Connector:

CARD INSERTION STATE	DETECT AWITCH CIRCUIT STATE	SCHEMATIC
UNMATED	OPENED	DETECT PIN CD — C5
MATED	CLOSED	DETECT PIN CD — C5

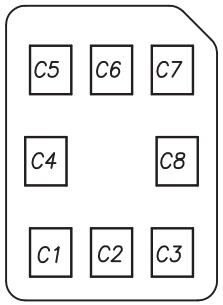
PAD AREA NO CIRCUIT AREA(TOP VIEW)

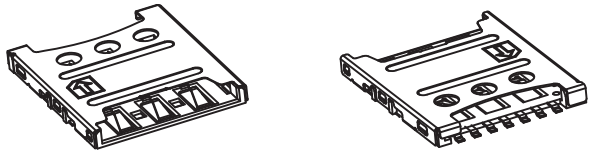


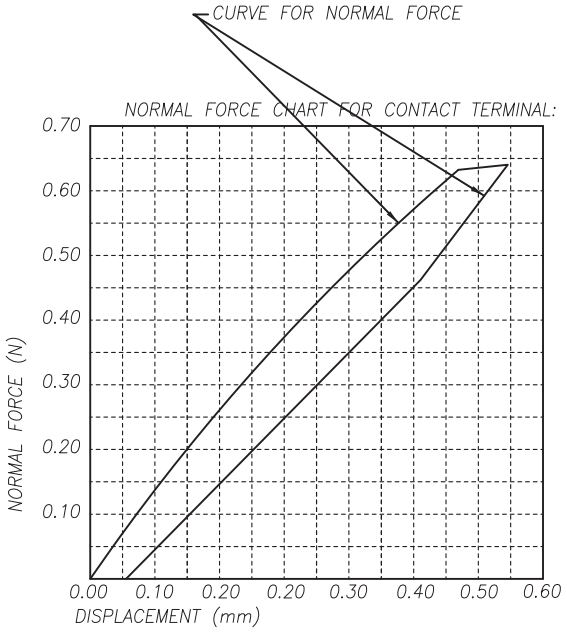
RECOMMENDED PCB LAYOUT TOP VIEW (TOLERANCE:±0.05)
RECOMMENDED MATAL MASK T=0.12MM

【 SIM Card Pin Assignments 】

PIN	Name
C1	Vcc
C2	RST
C3	CLK
C4	Reserved
C5	GND
C6	Vpp
C7	I/O
C8	Reserved







NOTES:
1. MATERIAL:SEE TABLE
2. SPECIALITY:
   2.1 Rated current: 0.5A Max
   2.2 Rated voltage: 30V AC
   2.3 Contact Resistance:100mohm MAX
   2.4 Insulation Resistance:1000Mohm MIN 500V DC
   2.5 Dielectric withstanding voltage: 500V AC/minute.
   2.6 Solder ability:250±5 10±0.5s.
   2.7 Durability:5000 Cycles
   2.8 Mating and Unmating force
   Insertion Force 10N Max
   Withdrawal Fore 0.5N Min

3. PART No.:

NS 0 DD - 12 00 06- 15 G

- NS: Nano SIM CON.
- 0: WITHOUT POST
- DD: WITH UPLOCK
- 12: HEIGHT 1.25mm
- 00: NORMAL TYPE.(ON PCB)
- 06: Pin NO. 6=6Pin
- 15: Product Order
- G: gold flash

NO.	PART NAME	Q'TY	DESCRIPTION	NOTE
3	TERMINAL	6	C5210-EH,T=0.12	Au 5u" PLATING
2	BASE INSULATOR	1	LCP+GF30%	BLACK
1	SHELL	1	SUS304-H,T=0.12	AU/Ni PLATING

UNLESS OTHERWISE SPECIFED TOLERANCES

DECIMALS: X :±0.5, X.X :±0.20, X.XX :±0.10
ANGLES: X :±2°, X.X :±1°

Wellco T&C Co., Ltd

TITLE	6Pin 1.25H PUSH PULL Nano SIM CARD CONN.		
DWN	ALLEN	PART NO. NSODD-120006-15G	
CHKD	KEVIN	SCALE:1:1	UNIT: mm
APVD	ERIC.W	SIZE: A3	SHEET:10F 1 REV: A2

CUSTOMER COPY